Fig. 1

Fig. 2

Fig. 3

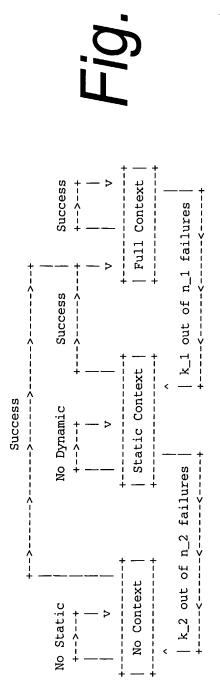
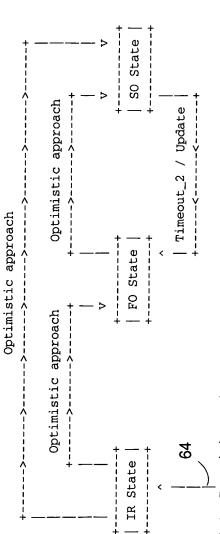


Fig. 6



State Transition Trigger (External signal from underlying system)

